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Subject: SUPPLEMENTAL RESPONSE TO NON-FINAL OFFICE
ACTION DATED JANUARY 26, 2005

Notes:

Attached hereto:

- 1) Supplemental Response to Non-Final Office Action Dated January 26, 2005
(6 pgs)

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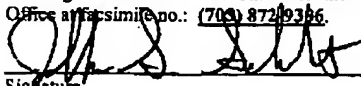
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Hung Cai Ngo	§ Group Art Unit: 2816
Serial No.: 10/733,933	§ Examiner: Luu, An T.
Date Filed: December 10, 2003	§ Confirmation No.: 8706
Title: Methods and Arrangements for a Low Power Phase- Locked Loop	§ IBM Docket No.: AUS920030779US1
	§ Atty Docket No.: (4019)

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SUPPLEMENTAL RESPONSE TO NON-FINAL OFFICE ACTION
DATED JANUARY 26, 2005

This paper is responsive to an agreement between the examiner and the Applicant in a telephone interview on May 2, 2005 and supplements the response filed on April 7, 2005 for Non-Final Office action dated January 26, 2005. In the interview Applicant agreed to add "OR gate logic" to independent claims 1, 17, and 21 to incorporate the allowable subject matter from original claim 5 into the independent claims. In accordance with the discussions with the examiner, Applicant understands that the addition avoids the necessity of further searches related to the patentability of the claims and places the claims in condition for allowance. Also, Applicant respectfully cancels claim 5 to avoid repetitive claim language. Based upon these amendments, Applicant respectfully requests that the application be advanced toward issuance.